

SEP. 27. 2006 1:09PM

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NO. 095 P. 2

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7590

06/30/2006

Peter J. Yim
 Morrison & Foerster LLP
 425 Market Street
 San Francisco, CA 94105-2482

09/28/2006 RNEBRAH1 00000041 031952 10087069

01 FC:1501 1400.00 DA
 02 FC:1504 300.00 DA
 03 FC:800 APPLICATION NO. 00 DA

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Valerie Cohen (Depositor's name)
 Valerie Cohen (Signature)
 September 27, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/087,069	02/28/2002	Nickhil Jakatdar	509982003200	5058

TITLE OF INVENTION: GENERATION AND USE OF INTEGRATED CIRCUIT PROFILE-BASED SIMULATION INFORMATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	10/02/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
DAY, HERNG DER	2128	703-013000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Timbre Technologies, Inc.

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

☒ Issue Fee
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☒ Advance Order - # of Copies 3

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Peter Yim

Date

September 27, 2006

Typed or printed name

Peter Yim

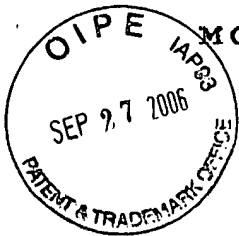
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44,417

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FROM: Peter J. Yim
Reg. No. 44,417**DATE:** September 27, 2006

Number of pages with cover page:	3	
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U.S. Patent Application Serial No. 10/087,069
For: GENERATION AND USE OF INTEGRATED CIRCUIT
PROFILE-BASED SIMULATION INFORMATION
By: Nickhil JAKATDAR et al.
Our Reference: 50998-20032.00

Attached is the following:

1. PTOL-85 (in duplicate, 2 pages)

sf-1910334

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